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			Application / Conf. No.	10/086,129 / 2232
TRANSMITTAL			Filing Date	February 28, 2002
FORM			First Named Inventor	David P. Schultz
(to be used for all correspondence after initial filing)			Examiner Name	James C. Kerveros
Mail Stop: AMENDMENT			Art Unit	2133
Express Mail			Patent No.	
Receipt No. Total Number of Pages in This Submission			Attorney Docket Number	X-1069 US
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PATENT

Conf. No.: 2232

IN THE UNITED STATES PATENT OFFICE

David P. Schultz

Assignee:

Xilinx, Inc.

Title:

Method and System for Flexibly Nesting JTAG TAP

Controllers for FPGA-Based System-On-Chip (SoC)

Serial No.:

10/086,129

File Date: 2/28/2002

Examiner:

James C. Kerveros

Art Unit:

2133

Docket No.:

X-1069 US

Conf. No.: 2232

Mail Stop AMENDMENT COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO SECOND OFFICE ACTION

Dear Sir:

In response to the Second Office Action mailed from the Patent Office on January 11, 2005, please amend the application as follows.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.